

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	<b>SYSTEM AND METHOD FOR ARBITRATION BETWEEN SHARED PERIPHERAL CORE DEVICES IN SYSTEM ON CHIP ARCHITECTURES</b>							
Application Number :								
Date :								
First Named Applicant:            Serafino Bueti								
Confirmation Number:								
Attorney Docket Number:        BUR920040148US1								
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Michael J. LESTRANGE Registered Number: 53,207</td><td>/Michael J. LESTRANGE, Reg. #53,207/</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Michael J. LESTRANGE Registered Number: 53,207	/Michael J. LESTRANGE, Reg. #53,207/	Attorney
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Documents being submitted:	Files
us-fee-sheet	BUR920040148US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920040148US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	BUR920040148US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040148US1Assign1.tif BUR920040148US1Assign2.tif
us-request	BUR920040148US1-usrequ.xml us-request.dtd us-request.xsl
us-declaration	BUR920040148US1Dec1.tif
us-declaration	BUR920040148US1Dec2.tif
us-declaration	BUR920040148US1Dec3.tif
application-body	BUR920040148US1SPEC-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920040148US1drawing1of3.tif BUR920040148US1drawing2of3.tif BUR920040148US1drawing3of3.tif
us-declaration	BUR920040148US1Dec4.tif

**Comments**

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